

What is claimed is:

1. A liquid crystal display panel, comprising:
 - a plurality of gate lines;
 - a plurality of data lines crossing the plurality of gate lines;
 - a plurality of liquid crystal cells defined by the gate and data lines, wherein each liquid crystal cell comprises a thin film transistor at a crossing of the gate and data lines, and a pixel electrode connected to the thin film transistor at a first side portion;
 - a first parasitic capacitor formed between the plurality of data lines and pixel electrodes preceding adjacent ones of the plurality of data lines;
 - a second parasitic capacitor formed between the plurality of data lines and pixel electrodes succeeding adjacent ones of the plurality of data lines; and
 - a groove formed within a second side portion of the pixel electrode adjacent the plurality of data lines, wherein the second side portion is opposite the first side portion.
2. The liquid crystal display panel according to claim 1, wherein the liquid crystal cells include:
 - a first horizontal line of liquid crystal cells having thin film transistors connected to preceding ones of adjacent data lines; and
 - a second horizontal line of liquid crystal cells having thin film transistors connected to successive ones of adjacent data lines.

3. The liquid crystal display panel according to claim 2, wherein the first horizontal line and the second horizontal line are alternately arranged within the liquid crystal display panel, and wherein within a vertical line of liquid crystal cells, every other liquid crystal cell is a liquid crystal cell from the first horizontal line.

4. The liquid crystal display panel according to claim 2, wherein the first horizontal line and the second horizontal line are alternately arranged within the liquid crystal display panel, and wherein within a vertical line of liquid crystal cells, every two liquid crystal cells are liquid crystal cells from the first horizontal line.

5. The liquid crystal display panel according to claim 2, wherein within the first horizontal line, the groove is at the second portion of the pixel electrode adjacent a succeeding data line, and within the second horizontal line, the groove is at the second portion of the pixel electrode adjacent a preceding data line.

6. The liquid crystal display panel according to claim 2, wherein
within the first horizontal line, consecutive ones of the liquid crystal cells are charged with pixel signals having alternating, opposite polarities;

within the second horizontal line, consecutive ones of the liquid crystal cells are charged with pixel signals having alternating, opposite polarities; and

wherein within the first and second horizontal lines, consecutive ones of the liquid crystal cells arranged within a vertical line, are charged with pixel signals having alternating,

opposite polarities.

7. The liquid crystal display panel according to claim 1, wherein a side length of the second portion of a first pixel electrode having the groove facing an adjacent data line is substantially equal to a side length of a portion of a second pixel electrode facing the adjacent data line.

8. The liquid crystal display panel according to claim 1, wherein the groove minimizes a parasitic capacitance differential between the first parasitic capacitor and the second parasitic capacitor.

9. A liquid crystal display panel, comprising:

a plurality of gate lines;

a plurality of data lines crossing the plurality of gate lines;

a plurality of liquid crystal cells arranged in a matrix pattern defined by the crossings, wherein each of the plurality of liquid crystal cells includes a pixel electrode and a thin film transistor coupled between an adjacent gate line, an adjacent data line, and the pixel electrode, wherein an area of a side of a pair of pixel electrodes adjacent a portion of a single data line are substantially equal.

10. The liquid crystal display panel according to claim 9, wherein thin film transistors of consecutive ones of the plurality of liquid crystal cells arranged within a vertical

line are alternately coupled to adjacent ones of the plurality of data lines.

11. The liquid crystal display panel according to claim 9, wherein thin film transistors of every two consecutively arranged ones of the plurality of liquid crystal cells arranged within a vertical line are alternately coupled to adjacent ones of the plurality of data lines.

12. The liquid crystal display panel according to claim 9, wherein thin film transistors of consecutive ones of the liquid crystal cells arranged within a first horizontal line are coupled to preceding ones of the plurality of data lines.

13. The liquid crystal display panel according to claim 9, wherein thin film transistors of consecutive ones of the liquid crystal cells arranged within a second horizontal line are coupled to succeeding ones of the plurality of data lines.

14. The liquid crystal display panel according to claim 9, wherein consecutive ones of the liquid crystal cells arranged within a horizontal line are charged with data signals having opposite polarity.

15. The liquid crystal display panel according to claim 9, wherein a parasitic capacitance between the pair of pixel electrodes and the single data line are substantially equal.

16. The liquid crystal display panel according to claim 9, further comprising a groove formed in a portion of each of the pixel electrodes adjacent a data line opposite the adjacent data line.

17. The liquid crystal display panel according to claim 16, wherein the groove is formed in an upper portion of the pixel electrode.

18. The liquid crystal display panel according to claim 16, wherein the groove is formed in a lower portion of the pixel electrode.

19. The liquid crystal display panel according to claim 16, wherein the groove is formed in a middle portion of the pixel electrode.

20. The liquid crystal display panel according to claim 9, wherein the side of the pair of pixel electrodes has substantially the same length.

21. A liquid crystal display panel, comprising:
a plurality of consecutively arranged, substantially parallel data lines; and
a plurality of pixel electrodes arranged in a matrix pattern adjacent the plurality of data lines, wherein an area of side portions of the plurality of pixel electrodes adjacent the plurality of data lines is substantially equal.

22. The liquid crystal display panel according to claim 21, further comprising a cut-out portion at a portion of each of the pixel electrodes adjacent a data line opposite the adjacent data line.

23. The liquid crystal display panel according to claim 21, wherein the cutout portion is at an upper portion of the pixel electrode.

24. The liquid crystal display panel according to claim 21, wherein the cutout portion is at a lower portion of the pixel electrode.

25. The liquid crystal display panel according to claim 21, wherein the cutout portion is at a middle portion of the pixel electrode.

26. A liquid crystal display panel, comprising:

a plurality of consecutively arranged, substantially parallel data lines; and

a plurality of pixel electrodes arranged in a matrix pattern adjacent the plurality of data lines, wherein a parasitic capacitance at side portions of the plurality of pixel electrodes adjacent the plurality of data lines is substantially equal.

27. A method of making a liquid crystal display panel, comprising:

forming a plurality of consecutively arranged, substantially parallel data lines; and

forming a plurality of pixel electrodes arranged in a matrix pattern adjacent the

plurality of data lines, wherein a parasitic capacitance at side portions of the plurality of pixel electrodes adjacent the plurality of data lines is substantially equal.